

FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

Applicant(s): D.E. Parson et al.
 Case: 3-2-1-4
 Serial No.: 09/583,057
 Filing Date: May 30, 2000
 Group: TBA

2763

U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE	IF APPROPRIATE
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FOREIGN PATENT DOCUMENTS

EXAMINER	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION	YES	NO
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OTHER DOCUMENTS

EXAMINER	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
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- TS 1. D. Jaskolski, "How to Design an IDE for Debugging Multi-processor DSP Systems," Engineering, pp. 1-17, June 2000.
- TS 2. Institute of Electrical and Electronics Engineers (IEEE) Standard 1149.1, "IEEE Standard Test Access Port and Boundary-Scan Architecture," IEEE, New York, NY, October 1993.
- TS 3. Institute of Electrical and Electronics Engineers (IEEE) Standard 1149.1b, "Supplement to IEEE Standard Test Access Port and Boundary-Scan Architecture," IEEE, New York, NY, March 1995.

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Examiner

Date Considered

2/24/03

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- TB 1. L. Goudge, "Debugging Embedded Systems," ARM White Paper, www.arm.com, 6 pages, 1998.
- TB 2. ScanProgrammer Product Information, ASSET InterTech, www.asset-intertech.com, 37 pages, March 2000.
- TB 3. Advanced Tutorial: Using the MVP Multiprocessing Features, Chapter 3, www.ti.com, pp. 3-1 through 3-17, undated.
- TB 4. Using the Parallel Debug Manager, Chapter 11, www.ti.com, pp. 11-1 through 11-21, undated.
- TB 5. BDM/JTAG Debug Interfaces, http://www.abatronag.ch, 6 pages, February 2000.
- TB 6. Multi-ICE™ Interface Unit, wysiwyg://4/http://www.arm.com, pp. 1-4, 2000.
- TB 7. Summit-ICE PCI Emulator, http://www.wmdsp.com, 1 page, 1998.
- TB 8. Mountain-ICE/WS JTAG Emulator-SBus, http://www.wmdsp.com, pp. 1-2, 1998.
- TB 9. Mountain-ICE JTAG Emulator-PC-ISA, http://www.wmdsp.com, pp. 1-2, 1998.

Examiner Terry A. B.

Date Considered 2/24/03

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